

# AHMED SATTAR SAJIT

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## Education

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**California State University, Fullerton (CSUF)**, California, U.S.A.

Master of Science, Computer Engineering

(January 2017)

*Thesis: "FinFET Memory Cell Improvements for Higher Immunity Against Single Event Upsets," available online on "http://pqdtopen.proquest.com/doc/1868839425.html?FMT=ABS"*

**Certificate in Education: English Reading, Writing, and Speaking from (CSUF)**(December 2014)

**University of Technology, Baghdad**

Bachelor of Science, Control and Systems Engineering

(June 2010)

*First Rank, Dean's list*

## Publications and Research

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- **A. S. Sajit** and M. A. Turi, "SEU Tolerance of FinFET 6T SRAM, 8T SRAM, and DICE Memory Cells," *The 7th IEEE Annual Computing and Communication Workshop and Conference*, Las Vegas, U.S., 2017, pp [313-317]. Available online <http://ieeexplore.ieee.org/document/7868477/>
- "A comparative study of Graphene Transistors Over Bulk CMOS".
- "Designing and Implementation of Vending Machine Using FPGA Board by the Aid of Verilog Hardware Description Language".
- "Implementation of TestBench Circuit and Modeling a Stack-at-Fault for Four Bits Ripple Carry Adder Using VHDL".
- "Implementing of Steganography Algorithm Using Matlab".
- "Determining Financial Risk Based on Credit History using ID3 by the Aid of CLIPS".
- "Sub-Part of Image Identification Based on Neural Networks".

## Training Courses

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- Teaching Proficiency Exam (TPE) (2017)
- Modern Teaching Methods (MTM) (2017)
- Total Quality Management (TQM) (2014)

## Professional Associations

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- Iraqi Academics Syndicate
- Phi Beta Delta Honor Society Club, CSUF Chapter
- AEGEE-London Antennae

## Teaching Experience

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UoWasit

(March 2017 – Present)

### Teaching and other tasks

- Computer Interfacing and its Lab.
- Advanced Digital Techniques.
- Advanced Electronics for Computer Engineering
- Other tasks, including leadership of many teams in assessment and evaluation for the university.

### Pursuing my M.Sc. from (CSUF)

(March 2014 - March 2017)

- Advanced Digital Design Using Verilog\Grading and helping students.
- Computer Arithmetic Structure\ Grading and helping students.
- Designing with VHDL\ Grading and helping students.

UoWasit

### Teacher Assistant (Lab Demonstrator)

(December 2012- March 2014)

- Electronic Circuits Lab and Computer Logic and Architecture Lab
- Electronics/Grading and Helping Students
- Computer Logic and Architecture/Grading and Helping Students

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## Skills

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**Programming languages:** Python, C++, Matlab, NgSpice, VHDL, Verilog, SQL, and HTML

**Operating systems:** Windows, Linux, and Mac OS X platforms

**Hands-on experience:**

- Soldering teeny tiny electronic components (capacitors, resistors, and ICs); Working with FPGA boards, specifically, Xilinx Spartan® and Virtex® families.
- Winding of electrical machines, motors (up to five horsepower) and generators (up to 2 KV) as part of emerging green technologies initiative.

**Troubleshooting:** Acquired knowledge mainly about noise and interference in computer networks cabling projects.

## Workplace Experience

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**IT Engineer**

**(September 2011-March 2014)**

- Addressed software installation and operation questions that customers had
- Led many ministerial and university committees of at least three members
- Worked with diverse teams and prepared monthly, quarterly, and annually reports

**IT Assistant Engineer**

**(January 2010-September 2011)**

- Programmed more than 2500 code line
- Modified ten application software and coded in procedural and hardware languages.
- Designed and implemented hardware and developed over 25 control panels

**Technician**

**(January 2009-January 2010)**

- Simulated few control systems using Matlab™
- Performed control processes using Microprocessor coding in assembly language.
- Configured five designs of different control board like Microprocessor and Microcontroller.

## Community Service

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- Participating in many workshops held by the Iraqi Red Crescent Association.
- Teaching fundamental topics of Computer Science in the Continuing Learning Center at UoWasit.
- Served as a peer mentor during California State University student orientation week
- Worked in tutoring center of UoTech.

## Awards, Honors, and Certificate of Appreciations

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**Certificate of Appreciation for the participation in the International Conference on Advances in Sustainable Engineering and Applications Held by the col. of eng. at UoWasit.** (2018)

**California State University, Fullerton Recognized Student** (2017)

**Certificate of Presentation at IEEE CCWC-2017** (2017)

**Certificate of Appreciation** by the Iraqi minister of the Higher Education and Scientific research.

**Certificate of Appreciation** by the Iraqi inspector general office of the MOHESR.

**Certificate of Appreciation** by the president of the *UoALQADISIYAH*

**Certificate of Appreciation** by the president of the *UoWASIT*

**TOFIQ (Together for Iraq) Award of Academic Excellence** *Non-governmental Iraqi organization for supporting academia in Iraq, Washington D.C., USA.* (2016)

**Certificate of Academic Excellence and Achievements, The Iraqi Consulate, Los Angeles** (2016)

**Award of Excellence for Best Employee, UoWASIT** (2014)

**Award of Excellence for Best Employee, UoAL-QADISIYAH** (2013)

**Best Ten Senior Projects, UoTech** (2010)

## Grants

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- Travel expenses and pocket money by California State University, Fullerton (CSUF) for a research paper I presented in the IEEE conference 2017 held in Las Vegas. (January 2017)
- Merit based Fully funded scholarship by the Higher Committee for Education Development in Iraq (HCED).

(November 2010)